



## REMARKS

Claims 19-37 were examined. Claims 19, 20 and 31 are amended. Claims 1-18 and 21-22 are canceled. Claims 19-20 and 23-37 remain in the Application.

The Patent Office rejects claims 19-26, 28 and 31-35 under 35 U.S.C. §102(e). The Patent Office rejects claims 26-27, 29-30 and 36-37 under 35 U.S.C. §103(a). Reconsideration of the pending claims is respectfully requested in view of the above amendments and the following remarks.

### A. 35 U.S.C. §102(e): Rejection of Claims 19-26, 28 & 31-35

The Patent Office rejects claims 19-26, 28 and 31-35 under 35 U.S.C. §102(e) as anticipated by U.S. Patent No. 2004/0026765 of Currie, et al. (Currie).

Independent claim 19 is not anticipated by Currie, because Currie does not disclose an apparatus comprising a substrate, a strain-inducing layer, and a strained layer, wherein the strain-inducing layer and the strained layer are disposed in a channel region for a transistor device and a strain in the strained layer results from lateral contraction or expansion of the strain-inducing layer following formation of a gate electrode on the substrate. One way the apparatus may achieve the strain layer is from lateral contraction or expansion is by defining junction regions by removing a portion of the strained layer and possibly a portion of strain-inducing layer that may be formed in areas designated for junction regions using a gate electrode or a gate electrode with lateral spacers as an edge and then introducing material for the junction region. With reference to Figures 1-3, Currie describes a strain-inducing layer, a tensilely strained layer, and a compressively strained layer. With reference to Figure 3, Currie describes forming the source and drain contacts 340 by depositing a metal layer and reacting the metal layer with the channel layer and the relaxed SiGe layer 360. See col. 6, paragraph 0073, final sentence. Thus, it does not appear that Currie provides any opportunity to its strain-inducing layer to allow lateral contraction or expansion following introduction of a gate electrode.

Claims 20, 23-26 and 28 depend from claim 19 and therefore contain all the limitations of that claim. For at least the reasons stated with respect to claim 19, claims 20, 23-26 and 28 are not anticipated by Currie.

Independent claim 31 relates to a system comprising an integrated circuit package including a substrate, a strain-inducing layer disposed on the substrate, and a strained layer disposed on the strain-inducing layer, wherein the strain-inducing layer and the strained layer are disposed in a channel region for a transistor device and a strain in the strained layer results from lateral contraction or expansion of the strain-inducing layer following formation of a gate electrode on the substrate. Independent claim 31 is not anticipated by Currie, because Currie does not disclose a strain in the strained layer resulting from lateral contraction or expansion of the strain-inducing layer following formation of a gate electrode of the substrate. The discussion of Currie with respect to claim 19 is applicable to claim 31. Claims 34-35 depend from claim 31 and therefore contain all the limitations of that claim. For at least the reasons stated with respect to claim 31, claims 34-35 are not anticipated by Currie.

Applicants respectfully request that the Patent Office withdraw the rejection to claims 19-20, 23-26, 28 and 31-35 under 35 U.S.C. §102(e).

**B. 35 U.S.C. §103(a): Rejection of Claims 26-27, 29-30 & 36-37**

The Patent Office rejects claims 26-27, 29-30 and 36-37 under 35 U.S.C. §103(a) as obvious over Currie. Claims 26-27 and 29-30 depend from claim 1 and therefore contain all the limitations of that claim. Claims 36-37 depend from claim 31 and therefore contain all the limitations of that claim. For at least the reasons stated above with respect to their independent claims, claims 26-27, 29-30 and 36-37 are prima facie not obvious over Currie. Further, Currie does not provide any motivation for, for example, a strained layer resulting from lateral contraction or expansion of a strain-inducing layer. For the above stated reasons, Applicants respectfully request that the Patent Office withdraw the rejection to claims 26-27, 29-30 and 36-37 under 35 U.S.C. §103(a).



### CONCLUSION

In view of the foregoing, it is believed that all claims now pending, namely claims 17-27, are patentably define the subject invention over the prior art of record, and are in condition for allowance and such action is earnestly solicited at the earliest possible date. If the Examiner believes that a telephone conference would be useful in moving the application forward to allowance, the Examiner is encouraged to contact the undersigned at (310) 207 3800.

If necessary, the Commissioner is hereby authorized in this, concurrent and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2666 for any additional fees required under 37 C.F.R. §§ 1.16 or 1.17, particularly, extension of time fees.

Respectfully submitted,

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Date:

5/26/05

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Nedy Calderon

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5/26/05

Date